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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/420,635
	Filing Date	October 21, 1999
	First Named Inventor	Juengling
	Group Art Unit	1746
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1243

ENCLOSURES <i>(check all that apply)</i>		
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Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	David G. Latwesen, Ph.D. WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN, P.S.	Reg. No. 38,533
Signature		
Date	1/28/00	

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Inventor: Werner Juengling

Title: Semiconductor Processing Methods of Forming Devices on a Substrate, Forming Device Arrays on a Substrate, Forming Conductive Lines on a Substrate, and Forming Capacitor Arrays on a Substrate, and Integrated Circuitry

Assignee: Micron Technology, Inc.

Filed: October 21, 1999

Serial No.: 09/420,635

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the reference listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 1/28/00

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